

Attorney Docket No.: VLSI-3402.DIV.1 (US008032)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
Patent Application

Inventor(s): ANNAPRAGADA

Serial No.: 09/902,056

Group Art Unit: 2812

Filed: 7/9/01

Examiner: Not yet assigned

Title: A METHOD OF IMPROVING ADHESION OF CAP OXIDE TO NANOPOROUS SILICA FOR INTEGRATED CIRCUIT FABRICATION

Form 1449**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
AKS	L	EP 0687004A1	12/13/95	EPO	H01L	21/768	X	
AICS	M	EP 0881678A2	12/2/98	EPO	H01L	23/532	X	
AICS	N	EP 0884401A1	12/16/98	EPO	C23C	16/44	X	
AICS	O	WO 98/47177	10/22/98	PCT	H01L	21/768	X	
	P							
	Q							
	R							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	S	
	T	
Examiner	Asok Kumar Sarkar	
	Date Considered	7/25/02

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.